

Listing of the Claims

1. (Currently Amended) An apparatus comprising:
a first processor, the first processor adapted to:
identify a grid of pixels having a predetermined pattern of pixel values, the predetermined pattern optimized to reduce having an undesirable toner placement;
obtain a predetermined pcode grid from a memory ~~for, the predetermined pcode grid corresponding to the identified grid of pixels; and~~
send the predetermined pcode grid to a controller circuit for printing instead of the grid of pixels.
2. (Previously Presented) The apparatus recited in claim 1 wherein the first processor is a pcode processor.
3. (Original) The apparatus recited in claim 2 further comprising a second processor adapted to format print data to pixel data, the pixel data including pixel values.
4. (Previously Presented) The apparatus recited in claim 1 wherein the first processor is adapted to format print data to pixel data, the pixel data including pixel values.
5. (Original) The apparatus recited in claim 1 wherein the grid of pixels is a six-pixel by six-pixel grid.
6. (Previously Presented) The apparatus recited in claim 1 wherein the memory is a register internal to the first processor.
7. (Original) The apparatus recited in claim 1 wherein the memory is non-volatile but re-writable memory.
8. (Previously Presented) The apparatus recited in claim 1 wherein the memory is external to the first processor.
9. (Original) The apparatus recited in claim 1 wherein the predetermined pattern of pixel values and its corresponding pcode grid applies to a first color and a second predetermined pattern of pixel values and its corresponding pcode grid applies to a second color.

10. (Original) The apparatus recited in claim 1 wherein the apparatus is selected from a group consisting of a printer, a copier, and a multifunction appliance.

11. (Currently Amended) A method of printing data on a print medium, the method comprising:

identifying a grid of pixels having a predetermined pattern of pixel values, the predetermined pattern having an undesirable toner placement;
obtaining a predetermined pcode grid ~~for corresponding to~~ the identified grid of pixels, ~~the pcode grid being optimized to reduce undesired toner placement;~~ and
printing the predetermined pcode grid instead of the grid of pixels.

12. (Previously Presented) The method recited in claim 11 further comprising formatting printing data to pixel data, the pixel data including pixel values.

13. (Original) The method recited in claim 11 wherein the grid of pixels is a six-pixel by six-pixel grid.

14. (Previously Presented) The method recited in claim 11 wherein the pcode grid is obtained from a memory internal to a processor.

15. (Previously Presented) The method recited in claim 14 wherein the memory further comprises re-writable memory.

16. (Previously Presented) The method recited in claim 11 wherein the pcode grid is obtained from a memory external to a processor.

17. (Original) The method recited in claim 11 wherein the predetermined pattern of pixel values and its corresponding pcode grid applies to a first color and a second predetermined pattern of pixel values and its corresponding pcode grid applies to a second color.

18. (Currently Amended) An image forming apparatus comprising:

a memory for storing grid data for a plurality of predetermined patterns of pixel values ~~to reduce undesired toner placement, the predetermined patterns having an undesirable toner placement;~~

a processor in communication with the memory, the processor adapted to determine whether a set of received pixel data matches one of the predetermined patterns stored in the memory; and

a controller circuit in communication with the processor, the controller circuit for directing an output of an image in accordance with a grid data corresponding to one of the predetermined patterns, instead of the set of received pixel data.

19. (Previously Presented) The apparatus recited in claim 18 wherein the processor is a pcode processor.

20. (Previously Presented) The apparatus recited in claim 18 wherein the controller circuit further comprises a second processor adapted to format print data to pixel data, the pixel data including pixel values.

21. (Previously Presented) The apparatus recited in claim 18 wherein the processor is adapted to format print data to pixel data, the pixel data including pixel values.

22. (Previously Presented) The apparatus recited in claim 18 wherein the received pixel data corresponds to a six-pixel by six-pixel grid.

23. (Previously Presented) The apparatus recited in claim 18 wherein the memory is a register internal to the processor.

24. (Previously Presented) The apparatus recited in claim 18 wherein the memory is non-volatile but re-writable memory.

25. (Previously Presented) The apparatus recited in claim 18 wherein the memory is external to the processor.

26. (Previously Presented) The apparatus recited in claim 18 wherein the predetermined pattern of pixel values and its corresponding pcode grid applies to a first color and a second predetermined pattern of pixel values and its corresponding pcode grid applies to a second color.

27. (Previously Presented) The apparatus recited in claim 18 wherein the apparatus is selected from a group consisting of a printer, a copier, and a multifunction appliance.

28. (Currently Amended) An image forming apparatus comprising:

means for storing grid data for a plurality of predetermined patterns of pixel values to reduce undesired toner placement, the predetermined patterns having an undesired toner placement;

processing means, in communication with the means for storing, for determining whether a set of received pixel data matches one of the stored predetermined patterns; and

means of controlling image output, in communication with the processing means, for directing the output of the image in accordance with grid data corresponding to one of the predetermined patterns, instead of the set of received pixel data.

29. (Previously Presented) The apparatus recited in claim 28 wherein the processing means further comprises a pcode processor.

30. (Previously Presented) The apparatus recited in claim 29 wherein the means for controlling further comprises a second processor adapted to format print data to pixel data, the pixel data including pixel values.

31. (Previously Presented) The apparatus recited in claim 29 wherein the processor is adapted to format print data to pixel data, the pixel data including pixel values.

32. (Previously Presented) The apparatus recited in claim 28 wherein the grid data further comprises a six-pixel by six-pixel grid.

33. (Previously Presented) The apparatus recited in claim 28 wherein the means for storing is a register internal to the processing means.

34. (Previously Presented) The apparatus recited in claim 28 wherein the means for storing is a non-volatile but re-writable memory.

35. (Previously Presented) The apparatus recited in claim 28 wherein the means for storing is external to the processing means.

36. (Previously Presented) The apparatus recited in claim 28 wherein the predetermined pattern of pixel values and its corresponding pcode grid applies to a first color and a second predetermined pattern of pixel values and its corresponding pcode grid applies to a second color.

37. (Previously Presented) The apparatus recited in claim 28 wherein the apparatus is selected from a group consisting of a printer, a copier, and a multifunction appliance.